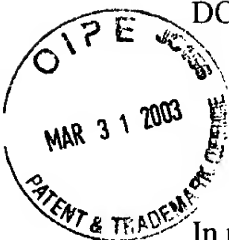


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#7/6410/A  
D. EVANS  
4-10-03

DOCKET NO. 03DV09036

PATENT APPLICATION  
SERIAL NO. 09/682,765



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Alan J. Janicek, et al.

Serial Number: 09/682,765

Filed: October 16, 2001

For: QUICK-CONNECT POSITIVE  
TEMPERATURE COEFFICIENT OF  
RESISTANCE  
RESISTOR/OVERLOAD  
ASSEMBLY AND METHOD

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§  
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Group Art Unit: 2832

Examiner: Karl Easthom

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

CERTIFICATE OF MAILING	
I hereby certify that the paper identified herein is being sent by first class mail, postage prepaid, to the Commissioner for Patents, United States Patent & Trademark Office, Washington, D.C. 20231, on the date indicated below:	
Date: 3-24-03	
Bradley D. Ellis	

AMENDMENT

Dear Sir:

In response to the Office Action dated November 22, 2002, for which a one month's extension of time is hereby requested, please consider the following amendments and remarks.

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**INTRODUCTORY COMMENTS**

The Office Action dated November 22, 2003 ("Office Action") has been carefully considered and the following amendments and remarks are presented in a sincere effort to put this application in condition for allowance. By this amendment, Claims 1-10 have been amended. Claims 11-21 remain withdrawn without prejudice pursuant to Applicants' October 22, 2002 Response to Restriction Requirement. Claims 1-10 are presently pending.